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APPLICANTS

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** CONTINUING DATA ***** *Kc*
none

** FOREIGN APPLICATIONS ***** *Kc*
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	STATE OR COUNTRY JAPAN	SHEETS DRAWING 20	TOTAL CLAIMS 41	INDEPENDENT CLAIMS 3
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EXAMINER'S SIGNATURE *Kc* INITIALS

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TITLE
 Shift register circuit including first shift register having plurality of stages connected in cascade and second shift register having more stages

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